

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) (37 CFR 1.98(b))	ATTY. DOCKET NO. 200301789-2 (1662-28001)	SERIAL NO. NOT YET ASSIGNED
	APPLICANTS David A.J. WEBB, Jr. et al.	
	FILING DATE CONCURRENTLY HEREWITH	GROUP UNKNOWN

U. S. PATENT DOCUMENTS													
Examiner Initial		Patent Number							Issue Date	Patentee	Class	Sub-class	Filing Date (If Appropriate)
H	AA	5	2	6	1	0	6	6	11/09/1993	Jouppi et al.	395	425	03/27/90
	AB	5	3	1	7	7	1	8	05/31/1994	Jouppi	395	425	01/25/93
	AC	5	7	5	8	1	8	3	05/26/1998	Scales	395	825	07/17/96
	AD	5	7	6	1	7	2	9	06/02/1998	Scales	711	148	07/17/96
	AE	5	7	8	7	4	8	0	07/28/1998	Scales et al.	711	148	07/17/96
	AF	5	8	0	2	5	8	5	09/01/1998	Scales et al.	711	154	07/17/96
	AG	5	8	0	9	4	5	0	09/15/1998	Chrysos et al.	702	186	11/26/97
	AH	5	8	7	5	1	5	1	02/23/1999	Mick	365	233	05/28/97
	AI	5	8	9	0	2	0	1	03/30/1999	McLellan et al.	711	108	07/01/97
	AJ	5	8	9	3	9	3	1	04/13/1999	Peng et al.	711	206	01/15/97
	AK	5	9	1	8	2	5	0	06/29/1999	Hammond	711	205	05/05/95
	AL	5	9	1	8	2	5	1	06/29/1999	Yamada et al.	711	207	12/23/96
	AM	5	9	2	3	8	7	2	07/13/1999	Chrysos et al.	395	591	11/26/97
	AN	5	9	5	0	2	2	8	09/07/1999	Scales et al.	711	148	02/03/97
	AO	5	9	6	4	8	6	7	10/12/1999	Anderson et al.	712	219	11/26/97
	AP	5	9	8	3	3	2	5	11/09/1999	Lewchuk	711	137	12/09/97
	AQ	6	0	0	0	0	4	4	12/07/1999	Chrysos et al.	714	47	11/26/97
	AR	6	0	7	0	2	2	7	05/30/2000	Rokicki	711	117	10/31/97
	AS	6	0	8	5	3	0	0	07/04/2000	Sunaga et al.	711	168	09/19/97
	AT	6	1	8	9	0	7	8	02/13/2001	Bauman et al.	711	156	12/22/98
	AU	6	0	1	4	7	2	8	01/11/2000	Baror	711	133	01/21/97
V	AV	6	0	3	8	6	5	1	03/14/2000	Van Huben et al.	712	21	03/23/98

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION												
			Document Number					Publication Date	Country or Patent Office	Class	Sub-Class	Translation
												Yes No

Heep T. Ng

1/8/06

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	APPLICANTS David A.J. WEBB, Jr. et al.	
	FILING DATE HEREWITH	GROUP

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)		
HW	AW	<i>Alpha Architecture Reference Manual</i> , Third Edition, The Alpha Architecture Committee, 1998 Digital Equipment Corporation (21 p.), in particular pages 3-1 through 3-15
	AX	<i>A Logic Design Structure For LSI Testability</i> , E. B. Eichelberger et al., 1977 IEEE (Pages 462-468)
	AY	<i>Direct RDRAM™ 256/288-Mbit (512Kx16/18x32s)</i> , Preliminary Information Document DL0060 Version 1.01 (69 p.)
	AZ	<i>Testability Features of AMD-K6™ Microprocessor</i> , R. S. Fetherston et al., Advanced Micro Devices (8 p.)
	BA	<i>Hardware Fault Containment in Scalable Shared-Memory Multiprocessors</i> , D. Teodosiu et al., Computer Systems Laboratory, Stanford University (12 p.), 1977
	BB	<i>Cellular Disco: resource management using virtual clusters on shared-memory multiprocessors</i> , K. Govil et al., 1999 ACM 1-58113-140-2/99/0012 (16 p.)
	BC	<i>Are Your PLDs Metastable?</i> , Cypress Semiconductor Corporation, March 6, 1997 (19 p.)
	BD	<i>Rambus® RIMM™ Module (with 128/144Mb RDRAMs)</i> , Preliminary Information, Document DL0084 Version 1.1 (12 p.)
	BE	<i>Direct Rambus™ RIMM™ Module Specification Version 1.0</i> , Rambus Inc., SL-0006-100 (32 p.), 2000
tb	BF	<i>End-To-End Fault Containment In Scalable Shared-Memory Multiprocessors</i> , D. Teodosiu, July 2000 (148 p.)
Examiner <u>Hept. T. V. W. J.</u> Date Considered <u>1/8/06</u>		
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		